

interconnect and temperature aware pdf

Interconnect Lifetime Prediction with Temporal and Spatial Temperature Gradients for Reliability-Aware Design and Runtime Management: Modeling and Applications

Interconnect Lifetime Prediction with Temporal and Spatial

A Thermally-Aware Energy Minimization Methodology for Global Interconnects Soheil Nazar Shahsavani, Alireza Shafaei, Shahin Nazarian, and Massoud Pedram ... interconnects vs. temperature in order to derive the optimal number and size of buffers for a given interconnect length

A Thermally-Aware Energy Minimization Methodology for

1 Interconnect Lifetime Prediction for Temperature-Aware Design UNIV. OF VIRGINIA DEPT. OF COMPUTER SCIENCE TECH. REPORT CS-2003-21 NOVEMBER 2003

Interconnect Lifetime Prediction for Temperature-Aware Design

interconnect and temperature aware pdf 1 Scope This test measures changes in resistance of plated-through hole barrels and internal layer connections as holes are subjected to thermal cycling.

Interconnect And Temperature Aware Unified Physical And

Interconnect and Thermal-aware Floorplanning for 3D Microprocessors — W.-L. Hung, G.M. Link, Yuan Xie, N. Vijaykrishnan, and M. J. Irwin The Pennsylvania State ...

Interconnect and Thermal-aware Floorplanning for 3D

temperature-aware floorplanning, and a temperature-aware floorplan optimizer. Using temperature-aware floorplanning, we eliminate the ... considers performance, temperature, area and interconnect length. They use a thermal model that considers the thermal and leakage inter-dependence for avoiding thermal runaway. Their solution con-

Temperature- and Cost-Aware Design of 3D Multiprocessor

requires global interconnect to be clocked at a low rate, which limits the achievable bandwidth and system performance. In this context, Optical Network-on-Chip (ONoC) is an

Thermal Aware Design Method for VCSEL-based On-Chip

temperature gradients can reduce the lifetime of a chip, and ... temperature-aware placement [4], [9]–[11]. Simplistically, if ... power consumption in cells and interconnects. The power consumption of cells is caused by dynamic, short-circuit and leakage currents, and the power consumption of intercon-

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